

09/486561

430 Rec'd PCT/PTO 29 FEB 2000

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Application Information

Title Line One:: SEMICONDUCTOR DEVICE AND METHOD OF
Title Line Two:: MANUFACTURE THEREOF, CIRCUIT BOARD
Title Line Three:: AND ELECTRONIC INSTRUMENT
Title Line Four::

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Total Drawing Sheets:: 7
Docket Number:: 105030

Continuity Information

>This application is a:: National Stage of
Application One:: PCT/JP99/03418
Filing Date:: June 25, 1999
Patent Number::
which is a::
>>Application Two::
Filing Date::
Patent Number::

Prior Foreign Applications

Foreign Application One:: 10-201246
Filing Date:: July 1, 1998
Country:: JAPAN
Priority Claimed:: YES
Foreign Application Two::
Filing Date::
Country::
Priority Claimed::
Foreign Application Three::
Filing Date::
Country::
Priority Claimed::